

**Notice of References Cited**

Application/Control No.

09/880,707

Applicant(s)/Patent Under  
Reexamination  
YELLIN ET AL.

Examiner

Joseph D. Torres

Art Unit

2133

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,516,437 B1	02-2003	Van Stralen et al.	714/755
	B	US-5,983,384 A	11-1999	Ross, John Anderson Fergus	714/755
	C	US-6,253,185 B1	06-2001	Arean et al.	704/500
	D	US-6,516,444 B1	02-2003	Maru, Tsuguo	714/795
	E	US-6,725,409 B1	04-2004	Wolf, Tod D.	714/755
	F	US-6,393,072 B1	05-2002	Ross et al.	375/340
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gerard J. M. Smit, Paul M. Heysters, Paul J. M. Havinga, Lodewijk T. Smit, Jaap Smit, and John Dilessen, Mapping the SISO module of the turbo decoder to a FPFA, In Proceedings of Second international symposium on Mobile Multimedia Systems & Applications (M
	V	Engling Yeo, Payam Pakzad, Borivoje Nikolic and Venkat Anantharam, VLSI Architectures for Iterative Decoders in Magnetic Recording Channels, IEEE TRANSACTIONS ON MAGNETICS, VOL. 37, NO. 2, MARCH 2001
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.